

Form PTO-1449  INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (Use several sheets if necessary)				Docket Number (Optional) 3351.8US (96-0223.07/US)		Application Number Not yet assigned	
				Applicant Brent Keeth		2818	
				Filing Date July 14, 2003		Group Art Unit Unknown	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
VN	6,392,303	05/2002	Keeth				
VN	6,429,529	08/2002	Keeth				
VN	6,456,518	09/2002	Keeth				
VN	6,504,255	01/2003	Keeth				
VN	6,594,173	07/2003	Keeth				

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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			Applicant	Brant K. Keith		
			Filing Date July 14, 2003			Group A/Unit Unknown
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
UN	5,940,321	08/1999	Takeuchi et al.			
UN	5,949,698	09/1999	Shirley			
UN	6,005,265	12/1999	Kuroda et al.			
UN	6,018,172	01/2000	Hidaka et al.			
UN	6,034,879	03/2000	Min et al.			
CP	6,043,562	03/2000	Keeth			
UN	6,084,307	07/2000	Keeth			
UN	6,094,370	07/2000	Takashima			Viet Q. Nguyen
UN	6,185,123	02/2001	Allen et al.			Primary Examiner
UN	6,188,598	02/2001	Mueller et al.			
UN	6,222,275	04/2001	Keeth			
UN	6,243,311	06/2001	Keeth			
<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						YES
<b>OTHER DOCUMENTS</b>						
(Including Author, Title, Date, Pertinent Pages, Etc.)						
UN	IBM Technical Disclosure Bulletin, Vol. 30, No. 3, August 1987, pp. 1314-1315.					
UN	IBM Technical Disclosure Bulletin, Vol. 30, No. 11, April 1988, pp. 246-248.					
EXAMINER			DATE CONSIDERED			
V. Nguyen			12/11/03			

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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
vn	5,280,443	01/1994	Hidaka et al.			
vn	5,315,542	05/1994	Melzner			
vn	5,396,450	03/1995	Takashima et al.			
vn	5,396,451	03/1995	Ema			
vn	5,499,205	03/1996	Ahn et al.			
vn	5,534,732	07/1996	DeBrosse et al.			
vn	5,581,126	12/1996	Moench			
vn	5,625,234	04/1997	Suzuki et al.			
vn	5,670,815	09/1997	Childs et al.			Viet Q. Nguyen Primary Examiner
vn	5,680,347	10/1997	Takeuchi et al.			
vn	5,815,428	09/1998	Tsuruda et al.			
vn	5,838,038	11/1998	Takashima et al.			
vn	5,864,181	01/1999	Keeth			
vn	5,877,976	03/1999	Lattimore et al.			

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

vn	Kimura et al., "A Block-Oriented RAM with Half-Sized DRAM Cell and Quasi-Folded Data-Line Architecture", November 11, 1991, IEEE, Journal of Solid-State Circuit, pps. 1511-1518.
vn	Takashima et al., "Open/Folded Bit-Line Arrangement for Ultra High-Density DRAMs", (Abstract), pps. 89-90.
vn	Hidaka et al., "A Divided/Shared Bit-Line Sensing Scheme for ULSI DRAM Cores", Journal of Solid-State Circuit, Vol. 26, No. 4, April 1991, pps. 473-477.

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V. Nguyen	12/11/03

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